

IN THE CLAIMS

Please amend the claims as follow:

1. (Currently amended) A reader/writer for sending/receiving a signal to/from the outside in a contactless manner through electromagnetic induction coupling, comprising:

a high voltage withstanding amplifier for amplifying an analog signal to be sent to the outside;

a plurality of resonance circuits for sending said analog signal amplified by said high voltage withstanding amplifier to the outside; and

a plurality of high voltage withstanding analog switching circuits provided correspondingly to said plurality of resonance circuits between an output of said high voltage withstanding amplifier and said plurality of resonance circuits, each for electrically connecting/disconnecting said high voltage withstanding amplifier to/from a corresponding one of said plurality of resonance circuits.

2. (Original) The reader/writer of claim 1, wherein each of said plurality of high voltage withstanding analog switching circuits includes:

a high voltage withstanding P-channel FET connected between a corresponding one of said plurality of resonance circuits and said high voltage withstanding amplifier;

a first resistor connected between a power supply node for receiving a positive voltage at a given level and a gate of said FET;

a second resistor connected between the gate of said FET and a ground node for receiving a ground voltage and having a resistance value smaller than a resistance value of said first resistor;

a diode connected between said second resistor and said ground node to be forward in a direction from said second resistor to said ground node; and

a switch serially connected to said second resistor and said diode between the gate of said FET and said ground node for electrically connecting/disconnecting the gate of said FET to/from said ground node.

3. (Original) The reader/writer of claim 2, wherein said FET is a junction FET, and said positive voltage is higher than a positive maximum value of an input signal to said FET.

4-5. (Cancelled)

6. (New) The reader/writer of claim 1, wherein each of said plurality of high voltage withstanding analog switching circuits includes:

a high voltage withstanding P-channel FET connecting between a corresponding one of said plurality of resonance circuits and said high voltage withstanding amplifier;

a switch connecting/disconnecting a positive power supply node and a first node;

a first resistor connected between said first node and a second node;

a second resistor connected between said second node and a negative power supply node;

and

a diode connected between said second node and a gate of said high voltage withstanding P-channel FET to be forward in a direction from said second node to said gate of said high voltage withstanding P-channel FET.

7. (New) The reader/writer of claim 1, wherein each of said plurality of high voltage withstanding analog switching circuits includes:

a high voltage withstanding P-channel FET connecting between a corresponding one of said plurality of resonance circuits and said high voltage withstanding amplifier;

a switch connecting/disconnecting a positive power supply node and a first node;

a first resistor connected between said first node and a second node;

a second resistor connected between said second node and a negative power supply node;
and

a diode connected between said second node and a gate of said high voltage withstanding P-channel FET to be reverse in a direction from said second node to said gate of said high voltage withstanding P-channel FET.